



Telelogic TAU[®] /Architect

Advanced visual modeling for Systems Engineers

TELELOGIC TAU[®]
GENERATION2

Meet the challenges of designing large, complex systems

Systems engineers today are under ever-greater pressure: Systems to be created tend to become larger and more complex, but still have to be delivered on time and within budget. Consequently, work on these projects simply must be right first time. Any option to refine the activities of the system engineer must be grasped and maximized - whether by improved techniques or through the automation offered by advanced tools.

As problems and solutions become more complex, it is harder to describe them, and more difficult for reviewers and other users to understand the descriptions. What's needed is a powerful way to describe both problems and solutions, which can provide levels of abstraction, and allow system engineers to focus on design and not implementation details.

Moreover, the more complex a system specification becomes the more difficult it is to detect errors within it.

What's needed is a way to increase formality and precision so that specifications are unambiguous. Then, users can rely on tools to automate checks for errors instead of expensive manual methods. What's needed is TAU/Architect.

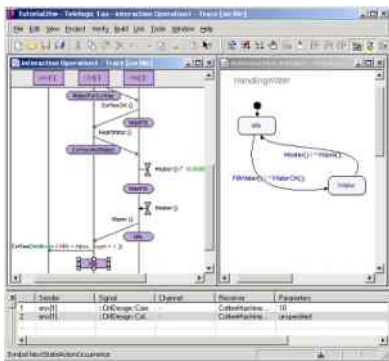
TAU/Architect is a modern, model-based system engineering tool for the analysis and specification of advanced systems. Using the latest industry standard visual modeling language, UML[™] (Unified Modeling Language[™]) 2.0, TAU/Architect supports comprehensive modeling of large and complex systems to create precise, easy-to-understand and unambiguous specifications. Uniquely, with TAU/Architect, system engineers not only specify the structure, but also the behavior of the system under development. What's more, the specifications can be simulated to verify and validate the system and demonstrate its behavior to end-users and other stakeholders at an early stage.

Visualize and verify your systems design

Visualize

TAU/Architect enables the rapid creation and maintenance of specification and design models. Thanks to its use of UML 2.0, TAU/Architect can provide a complete definition of system and subsystem architectures; modeling of workflow and responsibilities using activity diagrams; complex interactions between system/software components, including optional, conditional and repetitive sequences; and enable precise visual definition of system and software behavior using state machines.

Comprehensive and immediate syntax and semantic checking enables easy and early correction of mistakes. And, user-defined symbols can also be attached to UML stereotypes, enabling models to have a closer relationship to the real-world system under design and providing support for standards such as C4ISR/DoDAF.



TAU/Architect enables design models to be executed to verify their behavior. Execution traces can be viewed as UML sequence and state diagrams.

Verify

TAU/Architect offers a model verifier which compiles and executes UML models to provide an execution trace of the system. This allows early detection of errors in systems specifications before they are handed over to developers for implementation.

Bridge the gaps between requirements analysis, systems engineering and software development

Integration with Telelogic DOORS[®] /Analyst[®], gives visibility of textual and graphical (expressed in UML) requirements to the TAU/Architect user, and enables traceability to be quickly and easily maintained between high-level requirements and the system design model. TAU/Architect also shares a common tool environment and the UML 2.0 language with TAU/Developer[®], a tool designed for use by real-time and embedded software developers. Specifications visualized and verified with TAU/Architect can be refined and implemented using TAU/Developer.

The TAU/Architect Advantage:

- Express systems and software architectures completely and accurately through model driven development
- Divide and conquer using scalable modeling of complex architectures
- Detect and correct problems in requirements and systems specifications earlier with system simulation and full model verification
- Ensure systems and software specifications meet requirements with Telelogic DOORS
- Keep project documentation up-to-date easily with Telelogic DocExpress
- Use DOORS/Analyst, TAU/Architect and TAU/Developer together and bridge the gap between requirements analysts, systems engineers, and software developers

For more information about TAU/Architect and Telelogic's solutions for systems engineers, please visit www.telelogic.com
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